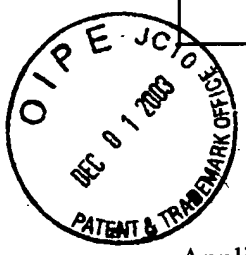
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 25, 2003.

Attorney for Applicant(s)



PATENT APPLICATION
Docket No. SLA1306

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Sachin G. Deshpande	
Serial No.:	10/637,408	
Filed:	August 7, 2003	Group Art Unit: 2126
For:	APPARATUS AND METHODS FOR PROVIDING COMMUNICATION BETWEEN SYSTEMS HAVING DIFFERENT PROTOCOL VERSIONS	

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Information Disclosure Statement disclosing information which has come to the attention of applicant and/or his attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), the enclosed Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicant nor his attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. § 102 or § 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, transmitted herewith are:

1. A completed copy of Form PTO/SB/08A "Information Disclosure Statement by Applicant" listing the patents, publications and other information being submitted for consideration; and

2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Form PTO/SB/08A, except for copies of U.S. patents and published U.S. patent applications which are not required for applications filed after June 30, 2003.

As this application was filed after June 30, 2003, copies of the U.S. patents and published U.S. patent applications listed on the enclosed Form PTO/SB/08A are not required and, therefore, not included herewith.

NON-ENGLISH INFORMATION

Pursuant to 37 C.F.R. § 1.98, following is a concise explanation of the relevance (as it is presently understood by the individual designated in 37 C.F.R. § 1.56(c) most knowledgeable about the content of the information), of each listed patent, publication or other information that is not in the English language:

1. Japanese Patent Application No. JP – 2001-285366 published October 12, 2001 discloses:

   To suppress a conventional problem that one IPv4 terminal dominates virtual IPv4 addresses and another IPv4 terminal can not obtain a virtual IPv4 address to be allocated to an IPv6 terminal in a communication mode in which the IPv6 address of the IPv6 terminal is specified by a virtual IPv4 address that are virtually assigned to an IPv6 terminal, thus enabling communication between the IPv4 terminal and the IPv6 terminal.

   When dynamically assigning a virtual IPv4 address to the IPv6 terminal, the IPv4 address of the IPv4 terminal in communication is also held, thus making it possible for each IPv4 terminal in communication to allocate the virtual IPv4 address to the IPv6 terminal.

2. Japanese Patent Application No. JP – 2000-253064 published September 14, 2000 discloses:

To reduce the required storage area in the case of conversion between Internal protocol version IPv4 and IPv6 by storing only the total of check sums of one fragment packet and each fragment packet.

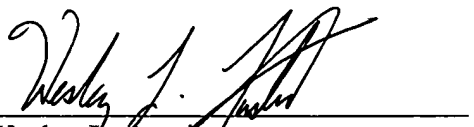
The IPv4-IPv6 converter is provided with a checksum re- calculation means 22 that calculates a check sum in a host layer protocol header of a fragment packet whose header is converted by a header conversion means 21, sums the result to a value having already been stored and stores the sum in a check sum storage section 32. When all fragment packets reach a transmission/storage control means 23, the control means 23 substitutes the check sum in the check sum storage section 32 to a host protocol check sum of fragment packets in a fragment packet storage section 33 and transmits the resulting fragment packets.

3. Japanese Patent Application No. JP – 11-136285 published May 21, 1999 discloses:

To attain communication with an IPv4 terminal even when an IPv4 address is not assigned to an IPv6 terminal in a permanent connection in advance.

The method is provided with an IP transmission reception means 10 that conducts transmission reception of an IPv4 packet and an IPv6 packet, an IP header conversion means 11 that converts between the IPv4 packet and the IPv6 packet, a DNS substitute means 13 that receives a domain information acquisition request sent from the IPv4 terminal or the IPv6 terminal to substitute the processing, an IPv4 address acquisition means 14 that acquires the IPv4 address from a DHCP server, and an IP address conversion information storage means 15 that stores an IPv6 address of the IPv6 terminal and the IPv4 address acquired by the IPv4 address acquisition means 14 in cross reference.

Respectfully submitted,



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	10/637,408
Filing Date	August 7, 2003
First Named Inventor	Sachin G. Deshpande
Group Art Unit	
Examiner Name	
Attorney Docket Number	SLA1306

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number: Number - Kind Code[2] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	U1	US-6,118,784	09/12/2000	Tsuchiya et al.	
	U2	US-2002/0136237	09/26/2002	Tsuchiya et al.	
	U3	US-2002/0136216	09/26/2002	Tsuchiya et al.	
	U4	US-2002/0024960	02/28/2002	Tsuchiya et al.	
	U5	US-2002/0021706	02/21/2002	Tsuchiya et al.	
	U6	US-2002/0021705	02/21/2002	Tsuchiya et al.	
	U7	US-2002/0021704	02/21/2002	Tsuchiya et al.	
	U8	US-2002/0021703	02/21/2002	Tsuchiya et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document: Country Code[3] - Number[4] - Kind Code[5] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
	F1	WIPO – WO 02/073933	09/19/2002	British Telecommunications		
	F2	JP – 2001-285366	10/12/2001	MATSUSHITA ELECTRIC IND CO LTD		
	F3	WIPO – WO 01/69887	09/20/2001	SRI International		
	F4	WIPO – WO 01/22683	03/29/2001	British Telecommunications		
	F5	EP – 1 087 575	03/28/2001	British Telecommunications		
	F6	JP – 2000-253064	09/14/2000	NEC SOFTWARE CHUGOKU LTD		
	F7	JP – 11-136285	05/21/1999	HITACHI LTD		
	F8	EP 0 840 482	05/06/1998	HITACHI LTD		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	O1	"Transition Mechanisms for IPv6 Hosts and Routers," R. Gilligan, pp. 1-26, August 2000.	
	O1	"Dual Stack Hosts Using "Bump-in-the-API" (BIA)," Seungyun Lee et al., pp. 1-14, April 2002.	
	O2	"Dual Stack Hosts Using "Bump-in-the Stack" Technique (BIS)," K. Tsuchiya et al., pp. 1-12, February 2000.	
	O3	"Stateless IP/ICMP Translation Algorithm (SIIT)," E. Nordmark, pp. 1-23, February 2000.	
	O4	"IPv4-IPv6 Dual Stack Hosts Using the "Bump-In-the-Stack" T chnique (BIS)," Yoshifumi Atarashi et al., IEICE Trans Commun., Vol. E84-B, No. 8, pp. 1996-2000, August 2001.	
	O5	"The Migration from IPv4 to IPv6," Chapter 12, pp. 227-240.	